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BSCpE 3A

**Relay Interface**

**VHDL Code:**

Library ieee;

Use ieee.std\_logic\_1164.all;

Use ieee.std\_logic\_unsigned.all;

Use ieee.std\_logic\_arith.all;

Entity relay is

Port (sw : in std\_logic;

Rl1,led : out std\_logic);

End relay;

Architecture behavioral of relay is

Begin

Rl1 <= sw;

Led <= sw;

End behavioral;